

G.L. Narasimha Murthy

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Professional Prologue

- A judicious leader with 14 years of qualitative experience in management, product development
- Currently associated with Infosys working as Senior Project Manager for Embedded Electronics at Engineering Services, managing multiple projects of global clients.
- Insightful experience in managing projects, with vital contributions towards assessment of technologies, and design of value propositions for customer requirements.
- Demonstrated expertise in digital and analog system design & troubleshooting, conducting requirement analysis for Electronic Projects, finalizing specifications, and interacting with clients for product development.
- Hands on Experience in:
 - Software, Hardware(PCB, Component, Simulation, Analysis) and Firmware Co development for different systems
 - Experience in different domains -Defence ,Avionics, Industrial Automation and Consumer Electronics
 - Reviewer and guide for system level architecture and deployment involving software, Hardware and Firmware
 - System level Managing experience from requirements to end product testing
 - Experience in Debugging and testing prototype products.
- A skilled communicator with strong team building & management, analytical, problem-solving and troubleshooting skills. Capacity to work under pressure situations and beat deadlines.
- Worked as key contributor for many proposals, contracts and as technical support to business teams

Organisational Experience

Infosys Technology Limited as Senior Project Manager-current

- Engaged in managing Embedded & Mobility Projects of Different global clientele
- Managing teams in software, and Firmware development for Engineering Integration

TATA Consultancy Services as Associate Consultant-1yr

- Engaged in managing hardware projects for one of the leaders in Media research for TV viewer ship monitoring
- Managing teams in software, Hardware and Firmware development for Engineering Integration

ICSA India Ltd, Hyderabad as AGM (R&D)-2yrs

- Engaged in managing hardware projects for one of the leaders in AMR and Metering Domain
- In charge for the Technical support for the Tenders and Technical Proposals and customization

Wipro Technologies, Hyderabad as Project Manager (Technical Consultant)-1.1yr

- Engaged in managing hardware projects for one of the leaders in Avionics domain.

TATA Consultancy Services, Hyderabad as Assistant Consultant-2.2yrs

- Involved as key technical coordination member at PMO of CMC-TCS synergy
- Involved in Management of embedded projects across the domains

Larsen & Toubro Limited, Mumbai -1yr

- Played a Assistant Manager for Transmitter design and concept proof for C&S band Radar Project delivered to ISRO

Bharat Electronics Limited, Hyderabad-5yrs

- Joined as Deputy Engineer
- Played a key role in the execution of Radar Project.

Competency Matrix

Managerial

-Project Management
-Resource Administration

-Strategy Planning
-Contingency Planning

-Roadmap Designing
-Risk Management

-Technology Evaluation
-Process Management

-Technology Administration
-Process Stabilisation

-Project Estimation
-Quality Assurance & Compliance

Functional

-Product Design & Development
-Troubleshooting
-Customer Relationship Management
-Technical Analysis

-Prototype Development
-Debugging
-Software/ Firmware Development
-Component Engineering

-Testing & Validation
-Product Standardisation
-Logic design
-Design Engineering

Technical Skill Set

Software

☞ OS : Linux, DOS 6.0 on PC-AT/386/486/Pentium, Windows XP/2000/NT/9X
☞ Networking : Client Server with TCP/IP, RS232/ 422/ 485, HART, Modbus
☞ Tools : MS Project, MS Office, AutoCAD, ORCAD, Visio, Altium Designer, PADS, Eclipse
☞ Languages : C, C++, Assembly, VHDL

Hardware :

☞ Intel 80x86, 80x51, PIC, Motorola 68XXX, and XILINX XQ4XFPGAs based Custom Designed Board, PALs, ADCs, DACs, Sensor Interfaces.
☞ Exposure with characterization of RF Modules (0.5-18GHz Los, Mixers, BPFs, Antennas)
☞ PCB Design ,EMI Analysis

Tools :

☞ Mentor and CADENCE Design tools for Schematic and analysis
☞ In Circuit Emulator (ICE), SDKs for Microcontroller Application development
☞ Logic Analyser (Agilent & Tektronix), Digital Storage Oscilloscope, Mixed Signal Oscilloscope, Pulse Generator, Waveform Generators, JTAG Emulators, BDM Emulators, RF Signal Generator, Spectrum Analyzer, Network Analyzer, XilinxISE, Leonardo Spectrum, Modelsim

Methods : ISO 9001:2000, SDLC, overview of CE, IEC standards

Domain Experience : Embedded Systems, Electronic Product Development

Education

☞ M.E. (Electronics & Communication Engineering) from Osmania University, Hyderabad
☞ B.Tech. (Electronics & Communication Engineering) (SKD University, Anantapur) (Secured 77%; rank holder).

Publications & Seminars

☞ Lecture on Wave Technology framework in Automotives at Embed Asia 2007.
☞ Devanagari Font Development procedure for POS Printers at TACTICS 2007.
☞ Development of Nano Medical Diagnostic equipment in/ at (Confirm whether e Health was a seminar or magazine) e Health in 2007
☞ Problems and Solutions in Physical layer Interfaces in Embedded Systems at TACTICS-2006.
☞ "Common Design Specific Re-usable" at Mentor Graphics USER2USER Conference in 2006.
☞ "Reducing of Clock Skews in FPGAs using Global Buffers" at Mentor Graphics USER2USER Conference in 2005.
☞ Analysis blocks creation for PCB Design Tools at Mentor forum
☞ Analysis of N-ISDN using Fiber Optic Interfaces at IEEE forum, Chennai in 1998.